

Team Name	Location	Job Title	Responsibilities	Requirements	Degree Qualification
Computer Vision team	London	Research Intern in Computer Vision	<ul style="list-style-type: none"> Working to solve challenges in 3D vision and computer graphics, computational photography, multi-modality learning, data efficient learning etc Conducting cutting edge research in computer vision, especially focusing on deep learning Collaborating with product groups in development of deep learning in computer vision technologies Collaborating with external partners in academia Participating in activities in academia and promoting the work conducted in the lab 	<p>List details of Knowledge, Skills, Experience and Qualifications needed to do the job:</p> <ul style="list-style-type: none"> PhD degree (preferred) in computer vision, or have equivalent research experiences Have strong research track record. Have published in top tier conferences including CVPR, ICCV, ECCV, NeurIPS, ICLR, Siggraph; and journals including IEEE TIP and TPAMI, etc. Demonstrate the ability to generate new ideas and innovate Have established themselves in the research communities and/or in the industry 	PHD Preferred
Reinforcement Learning	London	Research Intern in Reinforcement Learning	<ul style="list-style-type: none"> Working to solve challenges in the field of reinforcement learning, probabilistic modelling, or risk-averse learning Conducting cutting edge research in the field of reinforcement learning, probabilistic modelling, or risk-averse learning Collaborating with product groups in development of reinforcement learning, probabilistic modelling, or risk-averse learning Collaborating with external partners in academia Participating in activities in academia and promoting the work conducted in the lab 	<ul style="list-style-type: none"> PhD in Computer Science or a related field Strong research background demonstrated through journal and conference submissions in any of the following: ICML, NeurIPS, AISTATS, AAAI, UAI, IJCAI, JMLR, Annals of Statistics, and Annals of Probability Hands-on Experience in implementing reinforcement learning, probabilistic modelling, and/or multi-agent algorithms Ability to work in a diverse interdisciplinary team of researchers and engineers with different background 	PHD Preferred
AI theory	London	Research Intern in AI theory	<ul style="list-style-type: none"> Participate in cutting edge research aiming to solve fundamental machine learning problems. Design, implement and evaluate innovative solutions for data compression, few-shot learning and model selection problems. 	<p>List details of Knowledge, Skills, Experience and Qualifications needed to do the job:</p> <ul style="list-style-type: none"> PhD or MSc student in machine learning, or have equivalent research experiences. Experience publishing in top tier conferences is a plus. Experience in one or several fields related to the following topics: generative models, data compression, object detection, data-efficient learning, model selection. Hands on experience using Python and deep learning libraries. 	PHD Preferred
Natural Language Processing	London	Research Intern in NLP	<ul style="list-style-type: none"> Working to solve challenges in Machine Learning and Natural Language Processing, and collaboratively develop advanced products and services with other groups in the company. Conduct cutting-edge research in the fields of Natural Language Processing, Speech Recognition, and Speech Synthesis and Deep Learning. Develop AI-enabled products and services with other groups in the company. Collaborate with world-class organizations in academia. Participate in activities in academia, and promoting the work conducted in the lab. 	<ul style="list-style-type: none"> MSc or PhD student in NLP, Machine Learning, Computational Linguistics, or have equivalent research experiences We will value any publication top tier conferences including ACL, NAACL, EMNLP, NeurIPS, ICML, ICLR, etc. Prior experience or knowledge using Deeplearning frameworks such as Pytorch or Tensorflow and NLP frameworks such as Huggingface. Demonstrate the ability to generate new ideas and innovate 	PHD Preferred
CBG AppGallery team	London	Research Intern in Content Generation & NLP	<ul style="list-style-type: none"> Work to solve challenges and conducting cutting-edge research in NLP and content generation (e.g. lyrics generation, automatic book writing). Collaborate with product groups (e.g. Huawei Music, Huawei Books) in the development of content generation technologies for corresponding systems. Build benchmarks/baselines using public datasets. Dataset acquisition, cleaning, processing and augmentation. Building ML/DL models with novelty, applicability, and practicality in mind. Model evaluation and iteration. Publish research papers at the top-tier NLP/ML/AI conferences or journals. Carry out literature review and investigate the state-of-the-art frameworks or models for content generation systems. 	<ul style="list-style-type: none"> PhD degree preferred in Natural Language Processing with a focus on deep-learning models for Natural Language Generation. Have strong research track record and have published in top tier NLP/AI/ML conferences including ACL, NAACL, EMNLP, EACL, NeurIPS, ICLR, AAAI, ICML, WWW etc; and top tier journals. Demonstrate the ability to generate new ideas and innovate. Knowledge of audio/midi processing is a plus. 	PHD Preferred

CBG Multi Media team	London	Research Engineer in Computer vision	<ul style="list-style-type: none"> Working with the data scientists to research, develop, evaluate and optimize various problems in face analysis including landmark detection, face detection, segmentation and recognition, face reconstruction, neural rendering, etc. Collaborating with product groups in development of deep learning in computer vision technologies Deploying developed computer vision models on edge devices after optimization to meet customer requirements and maintain them to later improve to address additional customer requirements in future. 	<ul style="list-style-type: none"> MSc or PhD in Computer Science, Data Science, Machine Learning or in related fields preferred but candidates with Bachelor's degree in computer science are also welcome to apply provided they have a strong technical knowledge and experience in computer vision. Understanding about depth and breadth of computer vision and deep learning algorithms. Experience with any machine/deep learning frameworks like Tensorflow, Keras and PyTorch. Experience in training models through GPU computing using NVIDIA CUDA or on cloud. Ability to transform research articles into the working solutions to solve real-world problems. Strong experience in using both basic and advanced image processing algorithms for feature engineering. Proficiency in Python and related packages like numpy, scikit-image, PIL, opencv, matplotlib, seaborn, etc. Excellent written and verbal communication skills for effectively communicating with the team and ability to presenting information to varied technical and non-technical audience. Demonstrated ability to generate new ideas and innovate. <p>Ideal candidates should have (desirable)</p> <ul style="list-style-type: none"> A PhD degree in Natural Language Processing/Machine Learning or related areas, with a focus on LLMs. A strong research track record, and have published in top-tier NLP/AI/ML conferences including NeurIPS, ICLR, ICML, ACL, NAACL, EMNLP, EACL, and ton- 	Bachelor/Master
Compiler Lab	Cambridge	Compiler Engineer Intern	<p>Huawei compiler lab researches and develops latest compiler technologies to improve competitiveness of Huawei's computing platforms, including server and HPC. We aim to enhance productivity and performance of compilers. We develop open source compilers (GCC/LLVM) and contribute to communities. We arm ourselves with knowledge from processor architectures, software optimizations to programming models.</p> <p>Key Responsibilities</p> <ul style="list-style-type: none"> Develop LLVM/GCC based compilers Analyze & optimize performance of compiler generated code Use AI technology to help compiler optimize Design and implement compilers for GPU/CPU/DSP/AI; Explore the extreme performance optimisations. <p>Research on innovative compiler technologies and SW & HW co-design for general CPU.</p>	<ul style="list-style-type: none"> Passionate to develop compiler Advanced C/C++ programming skills Basic knowledge of CPU architecture Experience of contributing to open source projects Good at self-learning, courageous to explore new things, strong in practical skills Good communication skills and teamwork A current student study in Computing Science or related programmes; Have a good academic background and programming skills, published paper on top conferences or journals in the related fields (Preferred but not essential) 	All Degrees
Workload modelling team	Cambridge	Research Intern-CPU/NPU Performance Modelling	<ul style="list-style-type: none"> Develop tools for workload characterization, tracing and simulation. Engage in the investigation of cutting-edge, high-performance server CPU core and SOC architecture design, contributing vital data support for crucial decision-making processes. Analyse and extract the distinctive features of real-world scenario workloads, delivering essential insights to our in-house chip development department. 	<ul style="list-style-type: none"> C/C++, Python, Linux Experience in developing or using performance simulators like GEM5 (O3 model) Experience in developing or dynamic binary instrumentation infrastructures like QEMU, DynamoRIO or x86 PIN. Excellent collaboration and interpersonal skills Considered as a plus Knowledge of NPU/GPU architecture Experience in simulator development for NPU/GPU 	All Degrees
Turing Core	Cambridge/London	CPU Architect & Tooling Generalist	Depends on the skillsets	Knowledge on Computer Architecture, C++/C/Assembly, Python, Modelling, e.g. Qemu.	
Engineering Test Team	Ipswich	Software Automation Internship	<ul style="list-style-type: none"> Help establish configurable standardised reports from raw test data or test SQL DB Help software test team develop new test modules for integration, support & improve current test SW Help establish engineering test DoE's, data analysis and help compile reports 	<ul style="list-style-type: none"> Must be eligible to work in the UK without restriction for the duration of the internship BSc or higher degree in science, physics or related science Software skill such as C#, C++, LabVIEW, Python, SQL DB, etc Demonstrable knowledge/experience of software data analysis and reporting Flexible attitude to working hours. Excellent English communications skill-written and oral. Minimum internship duration of 9m 	BSc or higher degree

Reliability	Ipswich	Reliability & Failure Analysis Internship Intern	<ul style="list-style-type: none"> • Support Reliability test activates: assist in execute and measurements of burn-in, validation and qualification for a range of optoelectronic devices. • Data Analysis, review and reporting to reliability engineers. • Hands on failure analysis using internal resources and tools. • Root cause identification. • Assist setting up new test rigs and support existing test equipment maintenance. 	<ul style="list-style-type: none"> • Must be eligible to work in the UK without restriction for the duration of the internship • BSc or higher degree in physics or related physical science, electronics, communications, have already completed at least one year of degree. • Demonstrable knowledge/experience in the testing and measurement of optoelectronics devices. • Demonstrable knowledge/experience of data analysis. Skill using MS Excel. • Flexible attitude to working hours. • Excellent English communications skill-written and oral. 	BSc or higher degree
R&D	Ipswich	R&D Test Internship	<ul style="list-style-type: none"> • Support R&D Test Team activates: assist in execute test request to support development of new optoelectronic devices • Data analysis, review and reporting to R&D team • Assist & hands on experience on rig setup, operation, maintenance & trouble shooting • Opportunity to learn CAD & 3D printing to apply on improving rigs and/or prototyping new rigs 	<p>Required:</p> <ul style="list-style-type: none"> • Must be eligible to work in the UK without restriction for the duration of the internship • BSc or higher degree in physics or related physical science, electronics, communications, have already completed at least one year of degree. • Demonstrable knowledge/experience in the testing and measurement of optoelectronics devices. • Demonstrable knowledge/experience of data analysis. Skill using MS Excel. • Flexible attitude to working hours. • Excellent English communications skill-written and oral. • Minimum internship duration of 9m <p>Desired:</p> <ul style="list-style-type: none"> • Academic knowledge/experience in III-V semiconductor optoelectronic device in the following areas, i.e. design, simulation, fabrication and/or testing; • Software skill such as LabVIEW, Python, Minitab, etc. 	BSc or higher degree
R&D	Ipswich	Process Engineering Internship	<ul style="list-style-type: none"> • Micro project management of software developments • Produce clean, efficient code based on test and systems requirements • Verify and deploy version controlled release packages • Troubleshoot, debug and upgrade existing software • Create technical documentation for reference and reporting • Communicate and build specifications based on customer requirements. • Provide professional levels of support to end users 	<ul style="list-style-type: none"> • Development experience in Python programming and SQL DB • Remote control and automation of instrumentation - GPIB, USB, Ethernet, etc • Candidate must possess good verbal and written communication skills 	BSc or higher degree